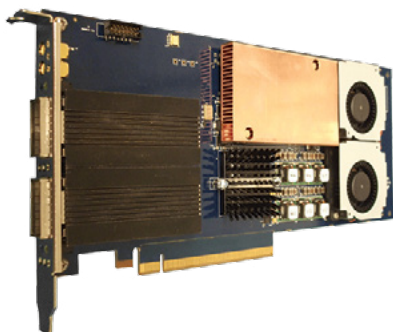


## Feature Summary

- Lossless Packet Capture
- Gigamon, Arista Timestamp
- Packet Merging
- Packet Parsing
- Tunneling Protocol Processing
- Packet Slicing
- Packet Filtering
- Packet Steering
- DMA (Direct Memory Access)
- Multi-core DMA
- Timestamp 4 nS
- RMON1 (RFC 2819) Statistics
- Onboard Sensors



## ANIC-200K4

### Dual 100GigE PCIe Network Accelerator with CFP4 Support

The ANIC-200K4 is a state of the art 100GE PCI Express NIC designed for demanding Network Monitoring and Security Applications. The ANIC-200K4 features Dual 100 GigE CFP4 Interfaces supporting LR4 and SR4 Modules.

The ANIC-200K4 highlights a unique scalable pipelined architecture implemented in a state-of-the-art FPGA supported by a high performance DDR3 Memory sub-system featuring TX-DMA capability operating on a Ring Buffer, Block Buffer or in Scatter-gather Mode across a 16 Lane PCIe, Gen 3 Bus.

The ANIC-200K4 offloads Host CPUs in monitoring applications demanding full 200 Gbps Lossless Packet Capture onto the adapter's memory buffers and 100 Gbps transfer across the PCIe bus into Host Buffers.

*In applications requiring Full 200 Gbps Lossless data Transfers into Host Buffers, two separate ANIC-200K4 NICs can be interconnected via a Direct Attached Cable (DAC) on Port 1 while receiving 100 Gbps of monitored traffic on Port 0 of each ANIC. The DAC connection between the ANIC cards enables an aggregate 200 Gbps of received traffic to be load balanced between the adapters and transferred across the PCIe bus into Host Buffers.*

Application Acceleration functions include Precise Time Stamping, Advanced Packet Filtering, Deduplication, De-Tunneling, Flow Classification and much more. The ANIC-200K4 presents data in a programmable organization of Buffers enabling load balancing to optimize Host Multi-Core CPU Resources.

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### Timing Sub-System

The ANIC-200K4 applies a time stamp to all captured packets to a resolution of 4 ns. The ANIC-200K4 offers a flexible time stamping reference which features a 1 PPS TTL serial interface via a mini-coax connecting to external GPS, GSM and CDMA timing sources.

### Software Support

The ANIC-200K4 is available complete with a software development guide including Linux and Windows Drivers and a comprehensive API featuring access to Hardware Health (Link Status, Onboard Temperature, Voltages, Power Good status) and embedded functions such as Filtering, Classification, Host Buffer and Time Stamping Sub-system control/configuration.

## Applications

- Passive and Inline Network Monitoring
- High Performance 100G Network Interface
- Network Forensics
- Network Test and Measurement
- Video Stream Monitoring and Analysis
- Packet Generator / Playback
- IDS/IPS & DDoS

## ANIC-200K4 Hardware Specifications

<b>PCI Interface</b>	16 lanes Gen 3 PCI Express
<b>Ethernet Compliance</b>	IEEE 802.ba 100GBASE-SR4, 100GBASE-LR4
<b>100G Connector</b>	CFP4 per CFP MSA Specifications CFP4 Optical supporting LR4 & SR4 Modules
<b>Time Stamping</b>	Resolution to 4 nS
<b>Timing Interfaces</b>	TTL External 1 PPS input via Mini-Coax & Repeater output
<b>Packet Memory</b>	12 GB
<b>Flash Memory</b>	8 MB
<b>Compliance</b>	EMI per FCC Part 15/EN 55022/VCCI/AS/NZS Immunity per EN 55024 RoHS
<b>Optional</b>	NEBS level 3 per GR-63 & GR-1089
<b>Power</b>	60 Watts, Auxiliary 12v Connector Provided
<b>Operating Temperature</b>	0 to 50 deg C
<b>Operating Humidity</b>	0 to 95% non-condensing
<b>Card Dimensions</b>	NON-NEBS: ¾ Length: 4.25 x 9.5 inches (107mm x 241mm) Designed for NEBS COMPLIANCE: Full length 4.25 x 12.28 (107mm x 312mm)