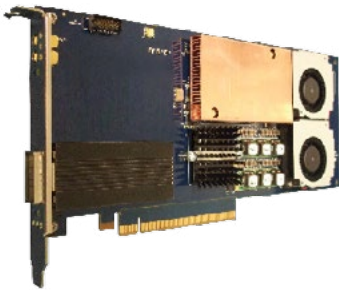


Feature Summary

- Lossless Packet Capture
- Gigamon, Arista Timestamp
- Packet Merging
- Packet Parsing
- Tunneling Protocol Processing
- Packet Slicing
- Packet Filtering
- Packet Steering
- DMA (Direct Memory Access)
- Multi-core DMA
- Timestamp 4 nS
- RMON1 (RFC 2819) Statistics
- Onboard Sensors



ANIC-100K4

100GigE PCIe Deep Packet Inspection Adapter

The ANIC-100K4 is a state of the art single port 100GigE FPGA based PCI Express Adapter designed for demanding Host Offload applications such as Network Monitoring and Security. The ANIC-100K4 features a CFP2 Interface which supports SR10, LR4 and ER4 Modules.

The ANIC-100K4 features a unique architecture based on a scalable pipelined architecture, implemented in a state-of-the-art FPGA supported by a high performance DDR3 Memory sub-system. In Monitoring applications, the ANIC-100K4 offloads Host CPUs in applications that demand full 100 Gbps Lossless Packet Capture onto the adapter buffers and 100 Gbps transfer across the PCIe bus into Host Buffers. The ANIC-100K4 also features a high performance TX-DMA capability which operates on a Ring Buffer or in Scatter-gather Mode.

Host offload functions for packet capture include Time Stamping, Packet Filtering, Packet Slicing and Hash based Classification. The ANIC-100K4 features a high performance DMA sub-system for efficient burst transfers of data across the 16 Lane, Gen 3 PCI Express Bus. The ANIC-100K4 presents data in a programmable organization of Ring Buffers which enable load balancing to optimize the use of Multi-Core CPU Resources

Timing Sub-System

The ANIC-100K4 applies a time stamp to all captured packets to a resolution of 4 ns. The ANIC-100K4 offers a flexible time stamping reference which features a 1 PPS TTL serial interface via a mini-coax which connects to external GPS, GSM and CDMA timing sources.

Software Support

The ANIC-100K4 is available complete with a software development guide which includes Linux and Windows Drivers and a comprehensive API that features access to Hardware Health (Link Status, Onboard Temperature, Voltages, Power Good status) and embedded functions such as Filtering, Classification, Host Buffer and Time Stamping Sub-system control/configuration.

Applications

- Passive and Inline Network Monitoring
- Network Latency Measurement
- Network Test and Measurement
- Video Stream Monitoring and Analysis
- Packet Generator / Playback
- Packet Aggregator
- Network Forensics
- Intrusion Detection
- Load Balancing
- High Performance Computing (HPC)

ANIC-100K4 Hardware Specifications

PCI Interface	16 lanes Gen 3 PCI Express
Ethernet Compliance	IEEE 802.ba 100GBASE-SR4, 100GBASE-SR10, 100GBASE-LR4, 100GBASE-ER4
100G Connector	CFP2 per CFP MSA Specifications CFP2 Optical supporting LR4 & SR4
Time Stamping	Resolution to 4 nS
Timing Interfaces	TTL External 1 PPS input via Mini-Coax & Repeater output
Packet Memory	6 GB
Flash Memory	128 MB
Compliance	EMI per FCC Part 15/EN 55022/VCCI/AS/NZS Safety per UL/CSA Immunity per EN 55024 RoHS
Optional	NEBS level 3 per GR-63 & GR-1089
Power	60 Watts, Auxiliary 12v Connector Provided
Operating Temperature	0 to 50 deg C
Operating Humidity	0 to 95% non-condensing
Card Dimensions	NON-NEBS: ¾ Length: 4.25 x 9.5 inches (107mm x 241mm) NEBS COMPLIANCE: Full length 4.25 x 12.28 (107mm x 312mm)