

Feature Summary

- Lossless Packet Capture
- Gigamon, Arista Timestamp
- Packet Merging
- Packet Parsing
- Tunneling Protocol Processing
- Packet Slicing
- Packet Filtering
- Deduplication
- Flow Classification
- Flow Based Filtering
- Packet Steering
- DMA (Direct Memory Access)
- Multi-core DMA
- PF_RING
- Timestamp 5.7 nS
- RMON1 (RFC 2819) Statistics
- Onboard Sensors



ANIC-80Ku

Dual 40GE/ 8 X 10GE PCIe Gen 3 Advanced Flow Processor

The ANIC-80Ku is an advanced PCIe Gen 3 flow processor featuring two QSFP+ Interfaces which support operations as a Dual 40GigE or 8 X 10GigE adapter designed for lossless packet capture and state of the art flow classification functions. Ideal for mission critical host CPU offload applications, the ANIC-80Ku is based on Accolade's next generation Advanced Packet Processor implemented in an FPGA supported by an 8 GB high performance DDR4 Memory sub-system.

The ANIC-80Ku is based on Accolade's next generation Advanced Packet Processor, implemented in a state of the art FPGA supported by a 8 GB high performance DDR4 Memory sub-system. Ideal for mission critical host CPU offload applications, the ANIC-80Ku is designed for lossless packet capture and transfer at 80 Gbps across the PCIe bus.

Advanced host CPU offload functions include Hash based Classification Application Aware Flow Management, Traffic Management and Packet De-duplication. The ANIC-80Ku's DMA Memory sub-system is designed for efficient burst transfers of data across the 8 lane Gen 3 PCI Express Bus. The ANIC-80Ku presents data in a programmable organization of Buffer Rings which enable load balancing and optimized use of Multi-Core CPU Resources.

Timing Sub-System

The timing sub-system in the ANIC-80Ku features an optional Temperature Controlled Crystal Oscillator (TCXO) as a stable high precision clock source. The ANIC-80Ku features a flexible timing sub-system which may be disciplined to NTP, IEEE-1588 and 1 PPS input from external GPS, GSM and CDMA timing sources.

Software Support

The ANIC-80Ku is available with a software development package and Linux Drivers and a comprehensive API that supports access to Hardware Health (PHY Status, Onboard Temperature, Voltages) and embedded functions such as Filtering, Classification, Host Buffer Management and Time Stamping Sub-system control/configuration.

Applications

- Passive TAP Monitoring
- Span and Mirror Port Monitoring
- In-Line Monitoring
- Network Latency Measurement
- Packet Generator / Playback
- Network Forensics
- Intrusion Detection

ANIC-80Ku Hardware Specifications

PCI Interface	8 lanes Gen 3 PCI Express
Ethernet Compliance	802.3aq for 10GbE and 802.3ba for 40GbE
Time Stamping	Resolution to 5.7 nS
Timing Interfaces	1 PPS Interface
Packet Memory	4 GB DDR4 Capture Buffer
Table Memory	4 GB DDR4
Compliance	EMI per FCC Part 15/EN 55022/VCCI/AS/NZS Immunity per EN 55024 RoHS
Power	Max 30 Watts without SFP+/QSFP+ Modules
Operating Temperature	0 to 50 deg C
Operating Humidity	0 to 95% non-condensing
Card Dimensions	4.25 x 6.5 inches / 107 x 165 mm